

IN THE SPECIFICATION

Please replace the paragraph beginning at page 12, line 16, with the following rewritten paragraph:

--In an actual implementation of the correction block as shown in FIG. 2, the common gain part can be omitted and the sinusoid can be generated by a look-up table 202. Look-up table 202 can reside in a Read-Only Memory (ROM) or other storage device. The gain and phase correction circuit 104 includes first 120 and second 122 input ports, and four multipliers 202-208 and two adders 210-212. A storage device such as a read-only memory (ROM) 202 has four output ports 214, 216, 218, 220 that provide the correct sinusoid to the respective multipliers 202, 204, 206 and 208, responsive to receiving the estimate signal for the gain 222 and phase 224 imbalance. The corrected I signal 124 is provided on a first output port, while the corrected Q signal 126 is provided in a second output port.--

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